

23. The method of claim 21 wherein said light modulating layer comprises a liquid crystal.

*A2
cont
Sub D2*

24. The method of claim 21 wherein said plurality of pixels are arranged in the form of a matrix.

25. The method of claim 24 wherein the step of addressing said pixels is performed in a line sequence.

26. A driving method for an electro-optical device having a plurality of pixels, each of which has a light modulating layer, said method comprising the steps of:

addressing said pixels with a scan signal for a predetermined period in sequence, where said predetermined period is time-divided into a predetermined number of divisions;

preparing an original image data in accordance with an image to be displayed;

converting said original image data into a data signal to be supplied to each of said pixels where said data signal contains a plurality of pulses, the number of said pulses being determined depending upon a tone of the image to be displayed;

supplying each of said pixels with said data signal during addressing with said scan signal for said predetermined period.

27. The method of claim 26 wherein voltage levels of said pulses are substantially constant.

28. The method of claim 26 wherein said light modulating layer comprises a liquid crystal.

*A2
cont
Sub
D3*

29. The method of claim 26 wherein said plurality of pixels are arranged in the form of a matrix.

30. The method of claim 29 wherein the step of addressing said pixels is performed in a line sequence.

31. An electro-optical device comprising:
a plurality of pixels arranged in a matrix form;
addressing means for addressing the pixels arranged in a row with a scan signal for a predetermined period, in sequence;
image data production means for producing image data in accordance with an image to be displayed;
image data processing means for processing said image data to produce a data signal having a plurality of pulses, the number of said pulses determined depending upon a tone of said image to be displayed;
data signal supply means for supplying said data signal to each of said pixels during addressing with said scan signal for said predetermined period.

32. The device of claim 31 wherein said pixels comprise a liquid crystal.

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33. The device of claim 31 wherein said image data production means includes a latch circuit.

*a2
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34. The device of claim 31 wherein said image data processing means includes a ROM table.

35. The device of claim 34 wherein said data signal supply means includes a flip-flop circuit and a counter.

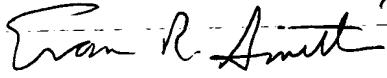
36. The device of claim 31 wherein said predetermined period is time-divided into a predetermined number of divisions.

37. The device of claim 31 further including video signal receiving circuits connected to drive said pixels, whereby said device forms a television display.

REMARKS

The present amendment submits new claims in this divisional application. Examination of these claims on the merits is requested.

Respectfully submitted,


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